

FIG. 1A

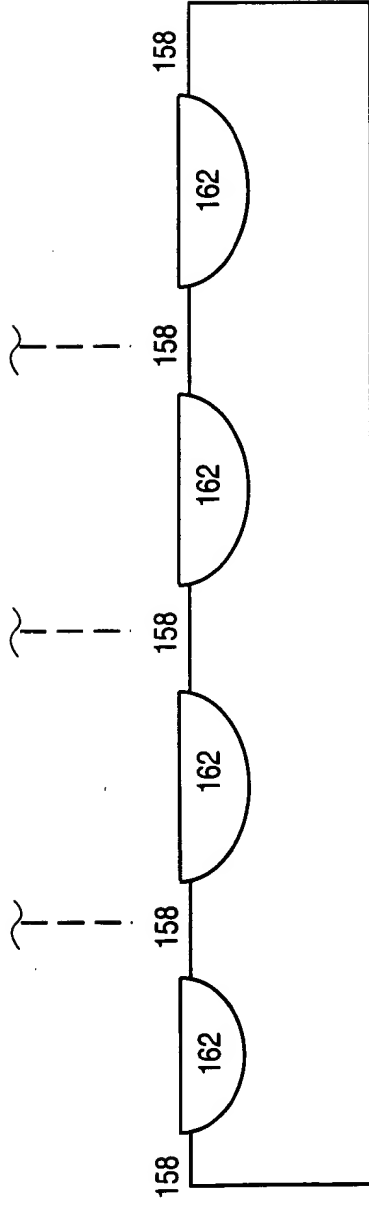


FIG. 1B

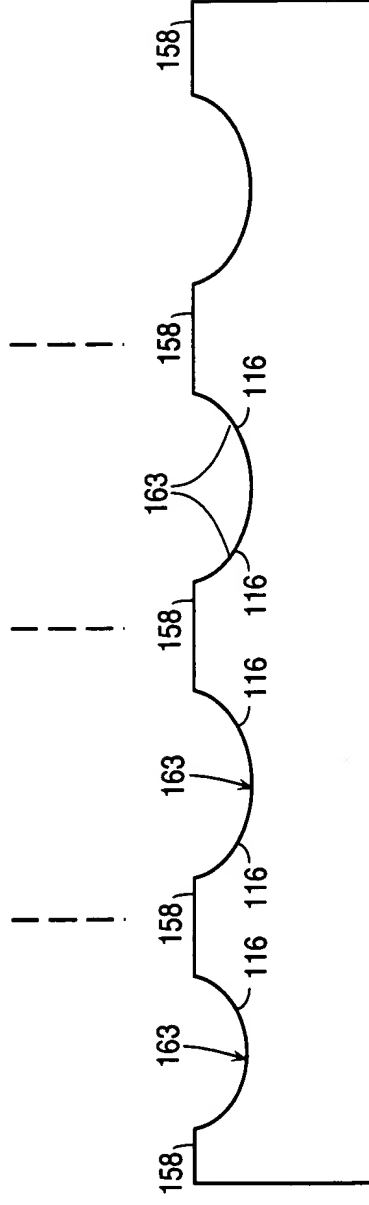


FIG. 1C

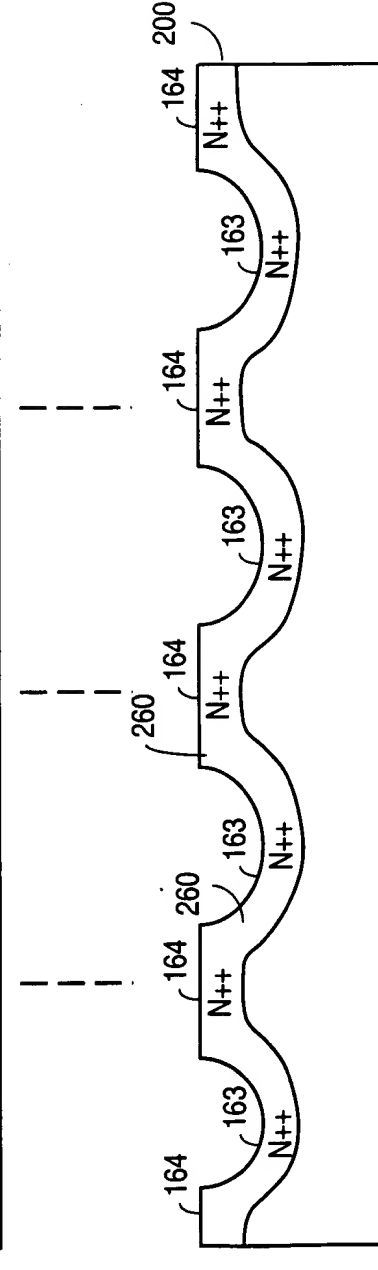


FIG. 1D

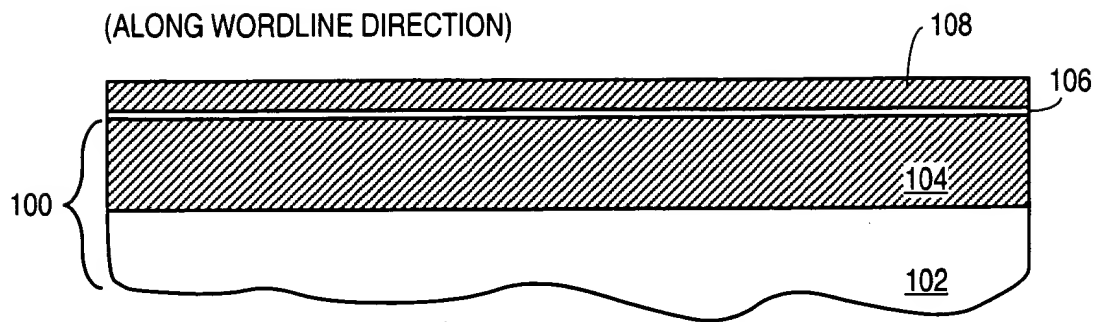


FIG. 2

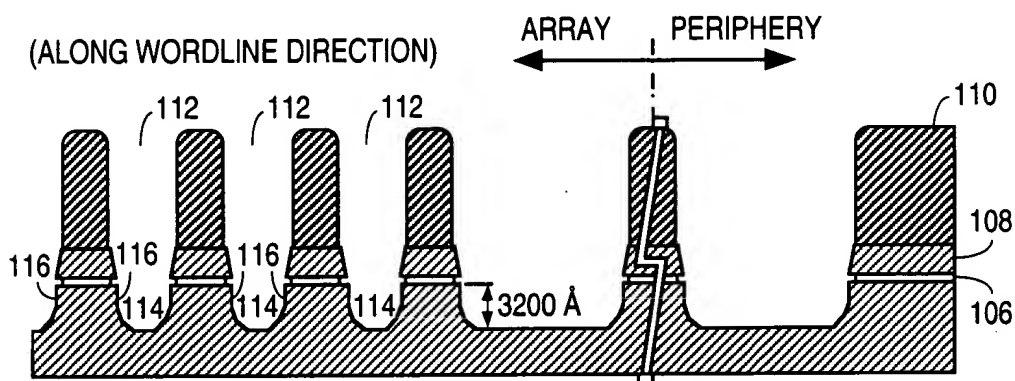


FIG. 3

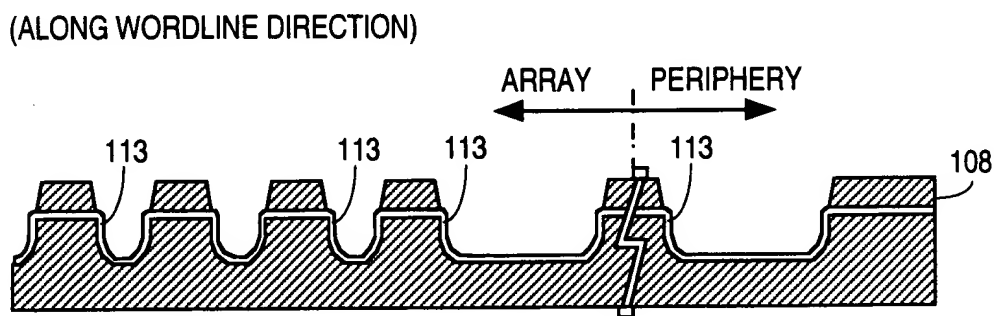


FIG. 4

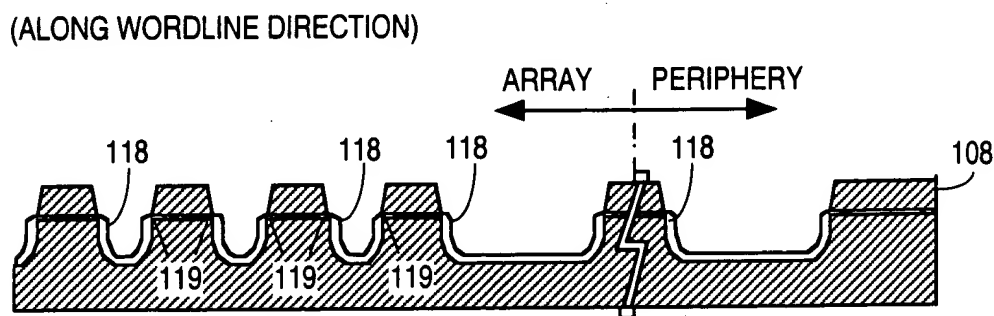


FIG. 5

(ALONG WORDLINE DIRECTION)

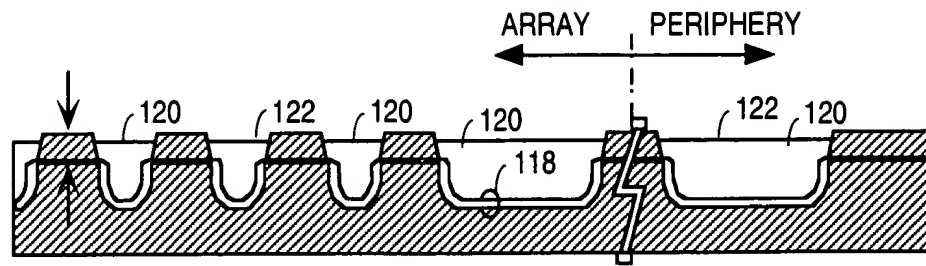


FIG. 6

(ALONG WORDLINE DIRECTION)

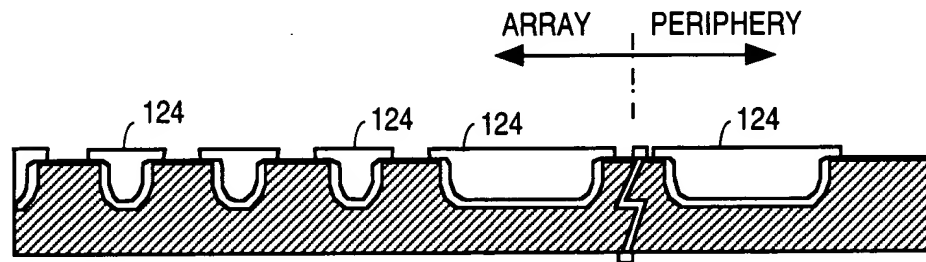


FIG. 7

(ALONG WORDLINE DIRECTION)

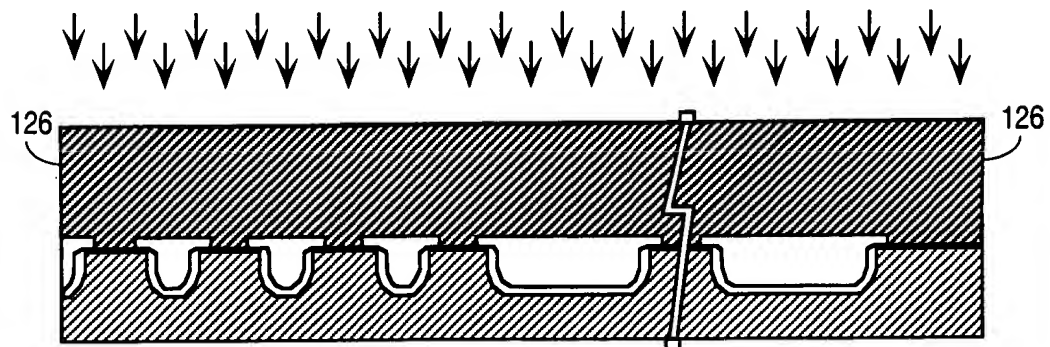


FIG. 8

(ALONG WORDLINE DIRECTION)

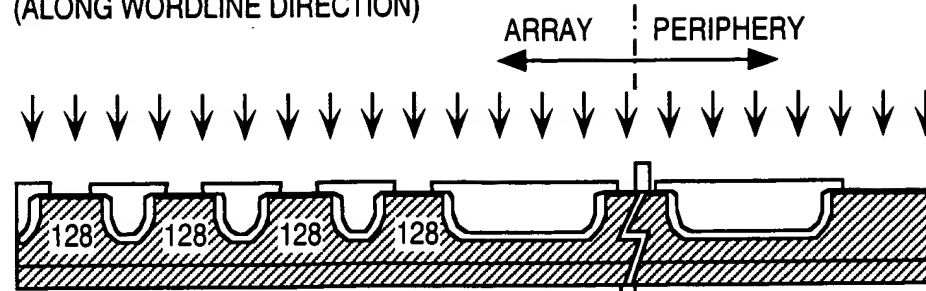


FIG. 9

(ALONG WORDLINE DIRECTION)

ARRAY PERIPHERY

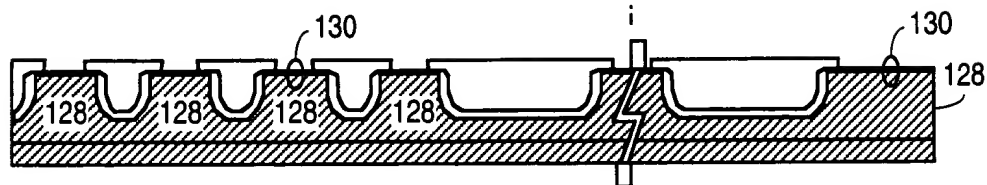


FIG. 10

(ALONG WORDLINE DIRECTION)

ARRAY PERIPHERY

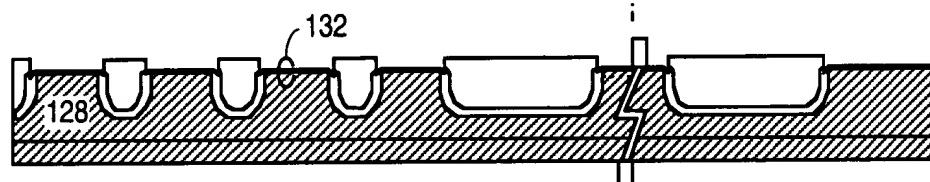


FIG. 11

(ALONG WORDLINE DIRECTION)

ARRAY PERIPHERY

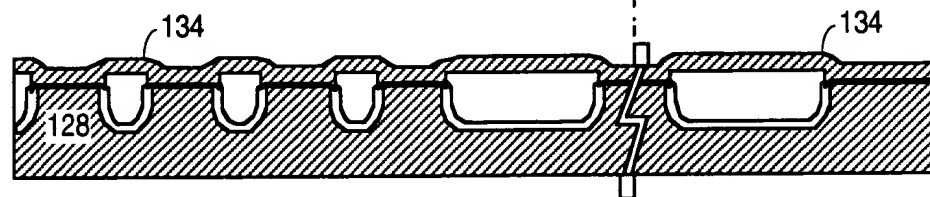


FIG. 12

(ALONG WORDLINE DIRECTION)

ARRAY PERIPHERY

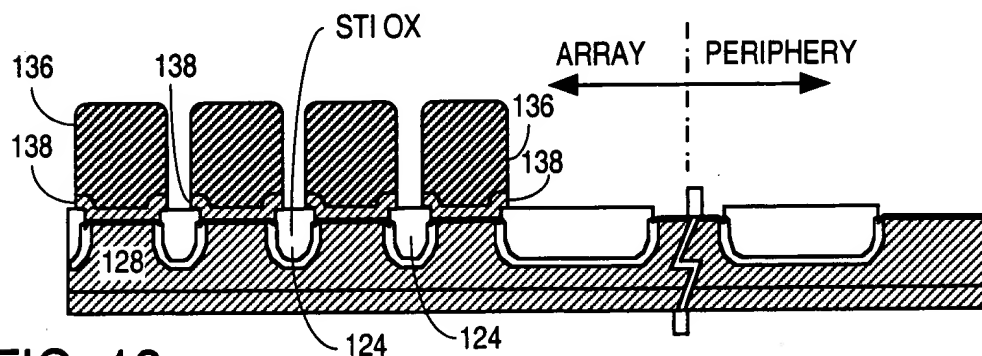


FIG. 13

(ALONG WORDLINE DIRECTION)

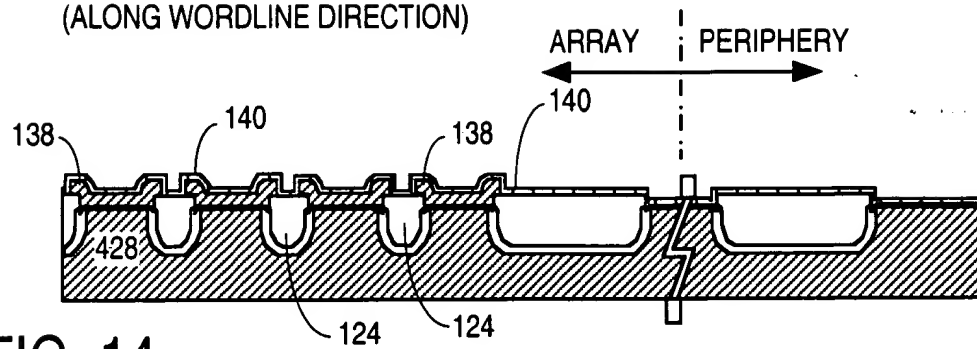


FIG. 14

(ALONG WORDLINE DIRECTION)

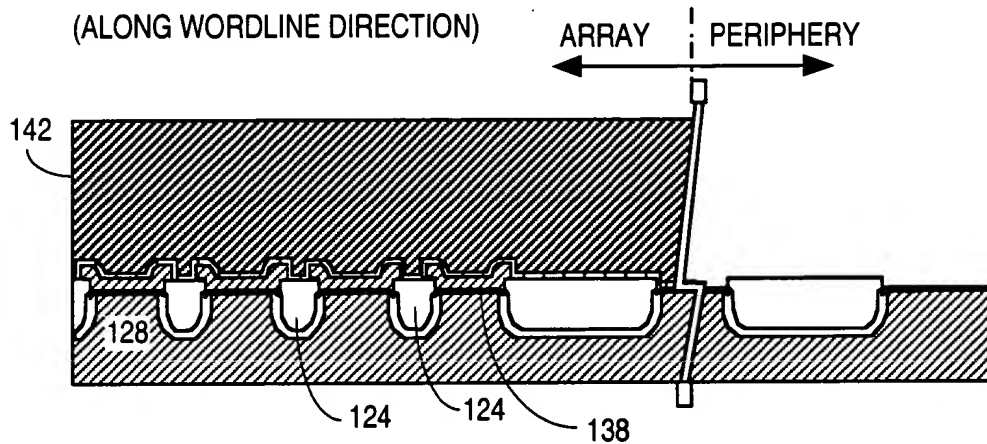


FIG. 15

(ALONG WORDLINE DIRECTION)

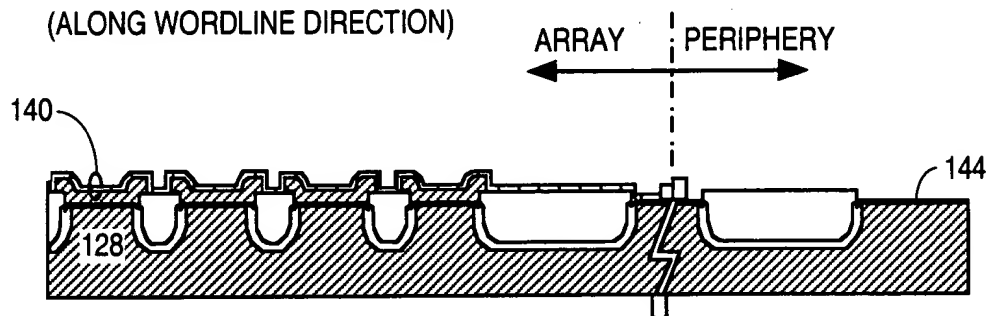


FIG. 16

(ALONG WORDLINE DIRECTION)

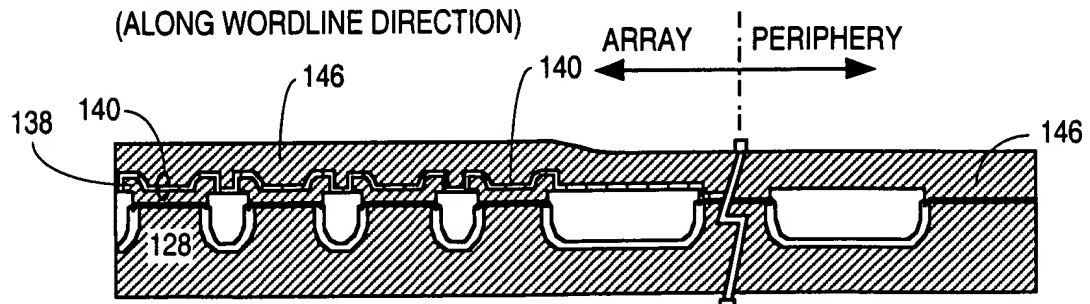


FIG. 17

(ALONG WORDLINE DIRECTION)

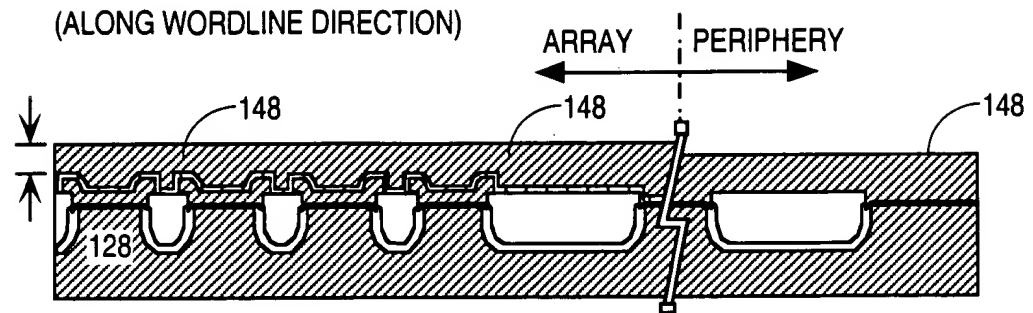


FIG. 18

(ALONG WORDLINE DIRECTION)

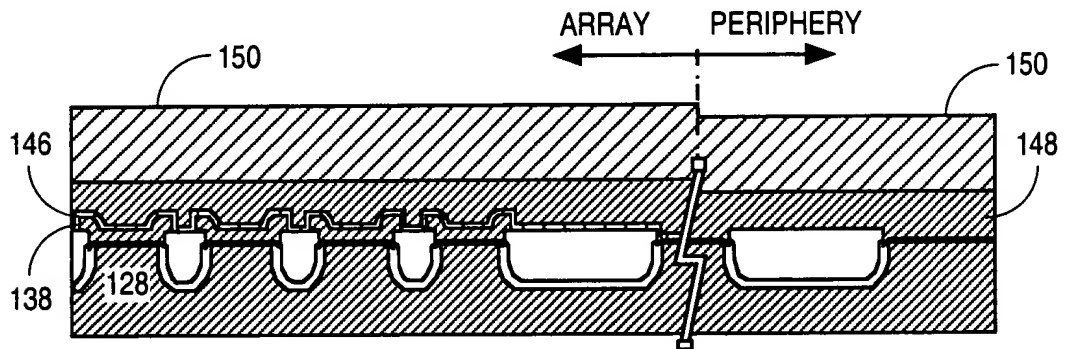


FIG. 19A

CROSS-SECTION ALONG BITLINE DIRECTION

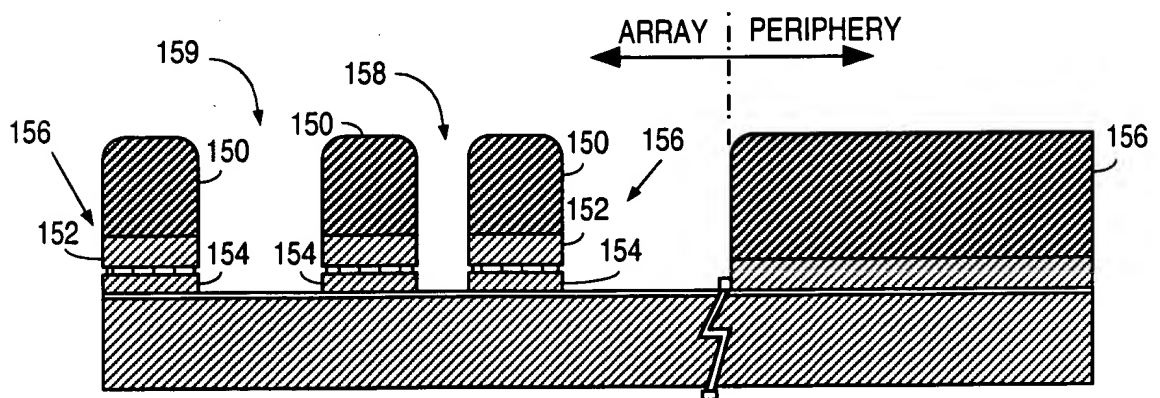


FIG. 19B

THROUGH S/D IN BITLINE DIRECTION

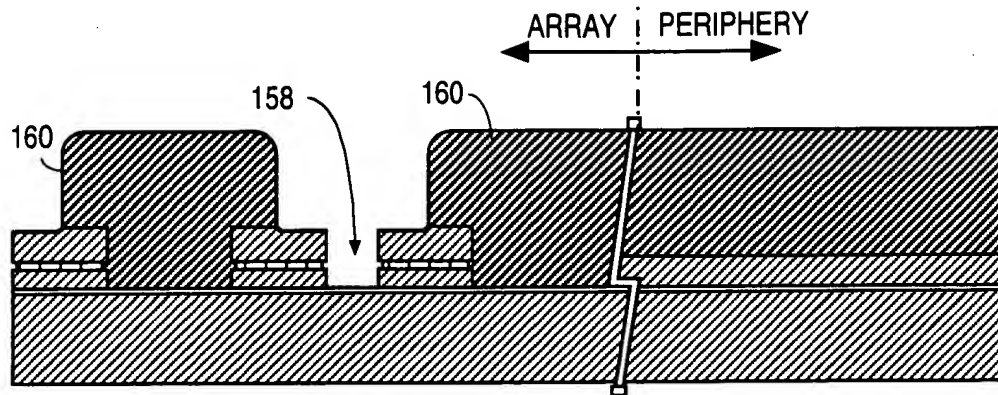


FIG. 20A

THROUGH STI IN BITLINE DIRECTION

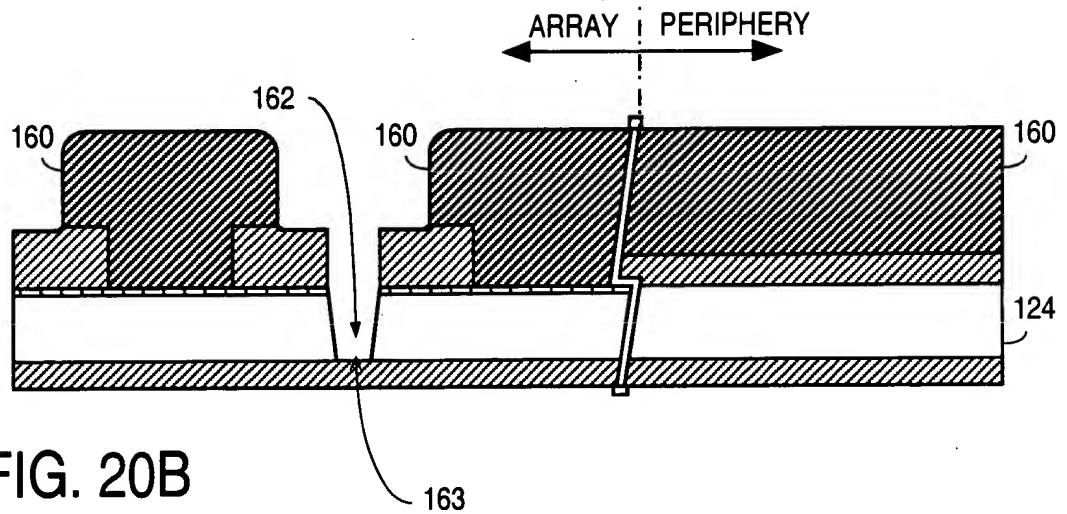


FIG. 20B

ALONG BITLINE DIRECTION

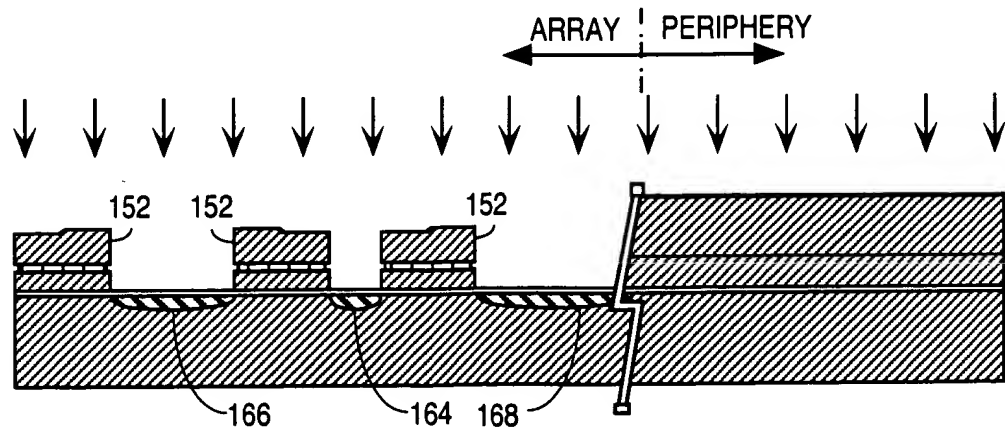


FIG. 21



ALONG BITLINE DIRECTION

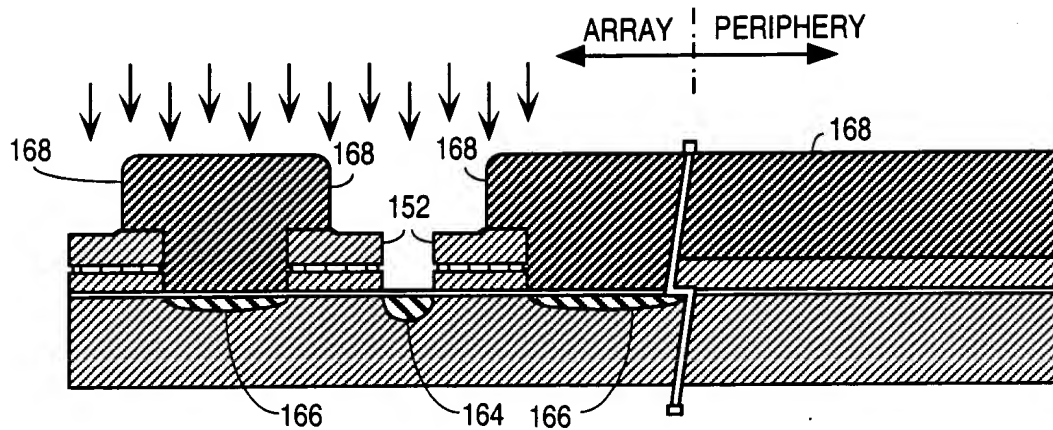


FIG. 22

ALONG BITLINE DIRECTION

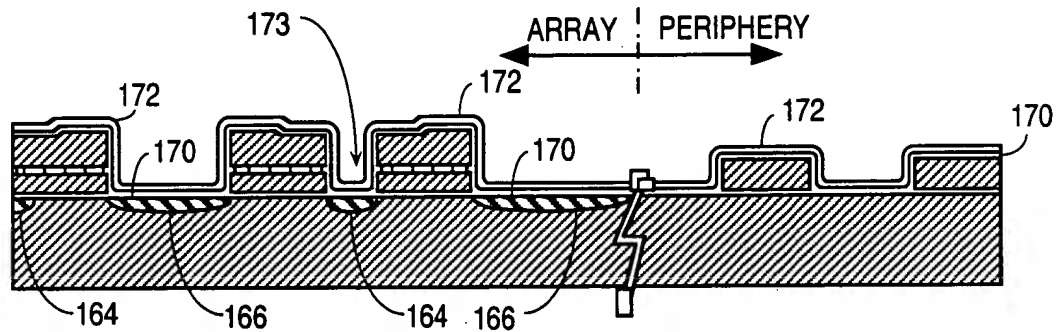


FIG. 23

ALONG BITLINE DIRECTION

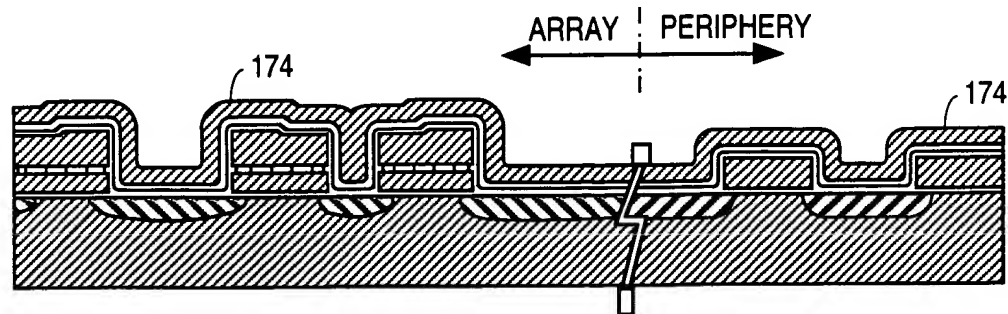


FIG. 24

ALONG BITLINE DIRECTION

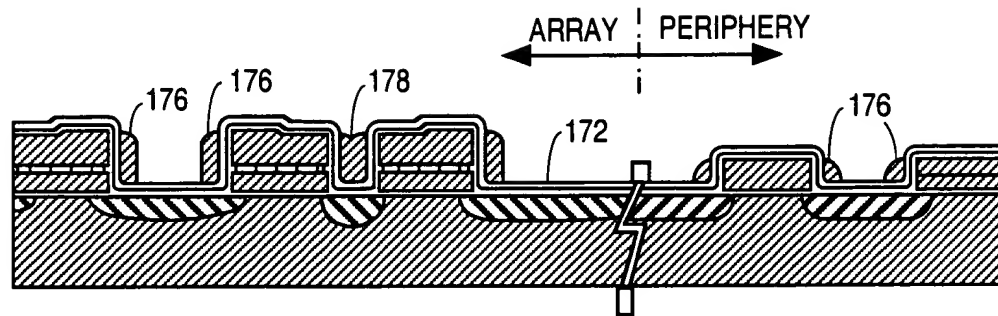


FIG. 25

ALONG BITLINE DIRECTION

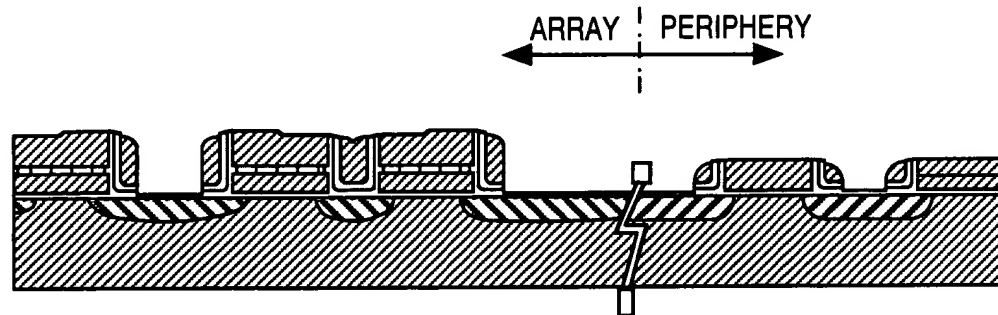


FIG. 26

ALONG BITLINE DIRECTION

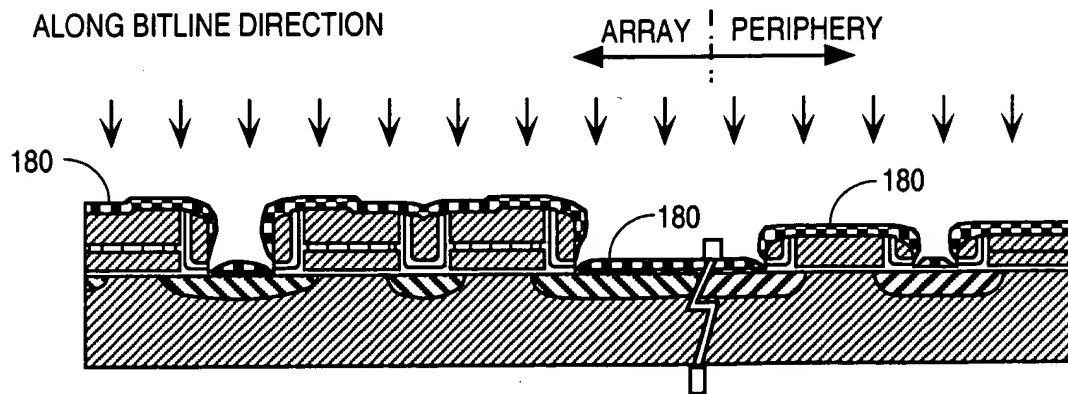


FIG. 27

FIG. 25 90059850

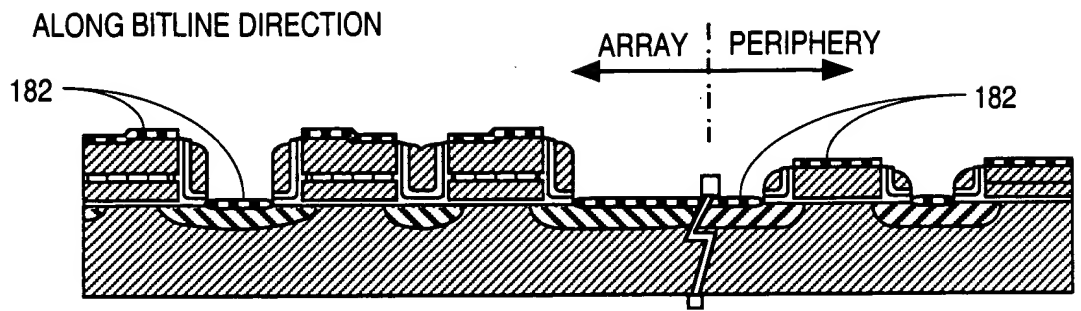


FIG. 28

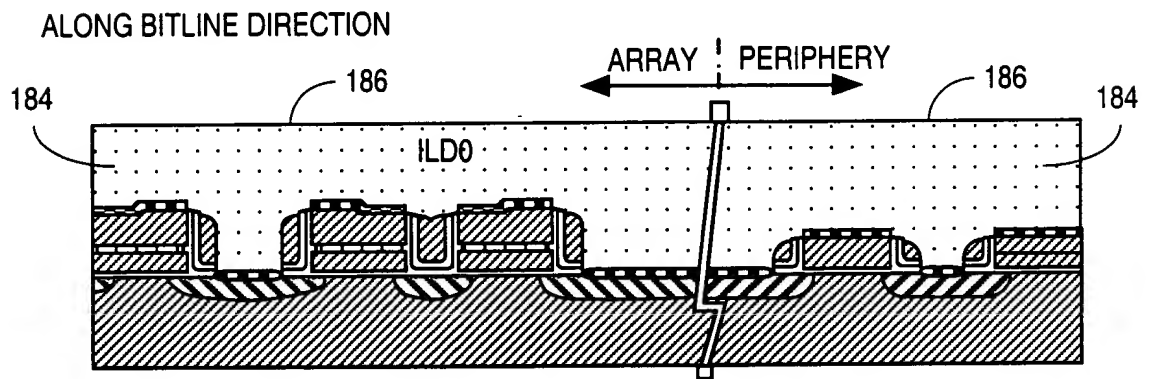


FIG. 29

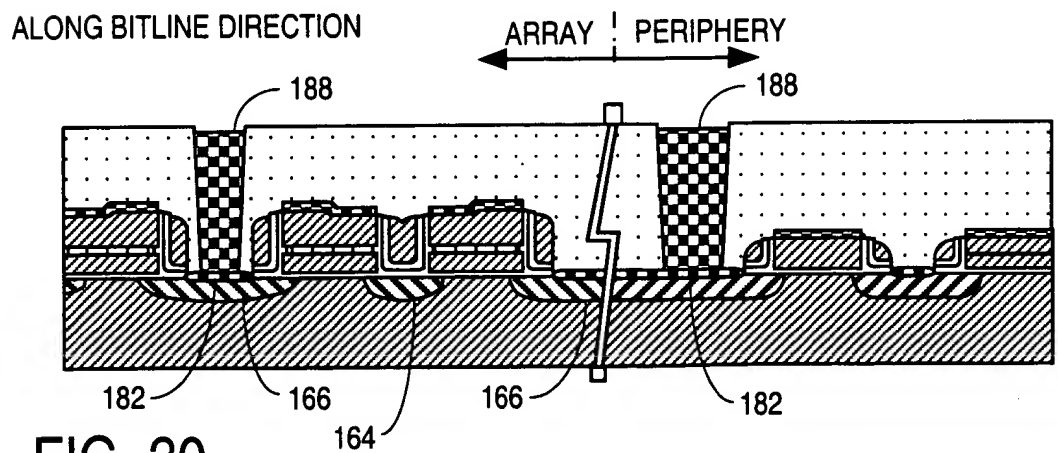


FIG. 30

ALONG BITLINE DIRECTION

ARRAY | PERIPHERY

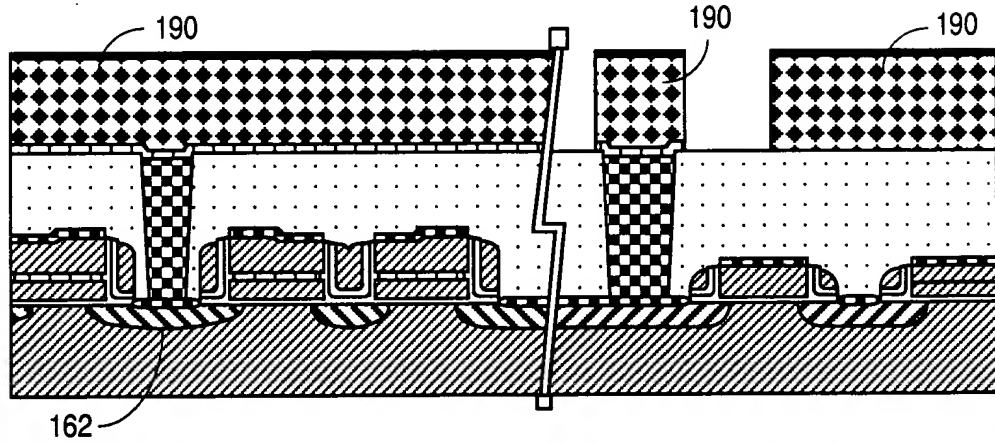


FIG. 31

FIG. 31